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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/735,555	12/14/2000	Kunihiro Itoh	108075-00022	6570

7590

04/16/2003

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EXAMINER

CHANG, DANIEL D

ART UNIT

PAPER NUMBER

2819

DATE MAILED: 04/16/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application No.

09/735,555

Applicant(s)

ITOH ET AL.

Examiner

Daniel D. Chang

Art Unit

2819

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 01 April 2003.
- 2a) ☒ This action is **FINAL**.                      2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-11 and 19-29 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-11 and 19-29 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on \_\_\_\_\_ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

**Priority under 35 U.S.C. §§ 119 and 120**

- 13) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All   b) ☐ Some \* c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

**Attachment(s)**

- |  |   |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892)                             | 4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s). _____  |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)         | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____ | 6) <input type="checkbox"/> Other:  |

***3 Acknowledgement***

Receipt is acknowledged of the Amendment filed April 1, 2003.

***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

Claims 1-11 and 19-29 are rejected under 35 U.S.C. 102(e) as being anticipated by Wert et al. (US 6,281,706 B1).

Regarding claims 5-11, 21-22, and 25-29, Wert teaches, in figures 7A, 7B and 8, an output buffer circuit comprising:

a first drive circuit, connected to an output terminal (305), for receiving an input signal (301) and generating a first output signal (output of 701/711) having a first state (logic HIGH/LOW);

a second drive circuit connected to the output terminal and having a lower output impedance than the first drive circuit (inherent, see col. 3, lines 8-18, col. 4, 3-42), wherein the second drive circuit generates a second output signal (output of 705/717) having the first state (logic state HIGH/LOW); and

a first control circuit (707/719), connected to the second drive circuit, for generating a first control signal (725/726) for driving the second drive circuit on the basis of the input signal and a delay signal (728/727) which is generated by adding a predetermined delay to the first

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output signal such that the second drive circuit generates the second output signal having the first state.

Method claims 1-4, 19-20, and 23-24 are essentially the same in scope as apparatus claims 5-11, 21-22, and 25-27 and are rejected similarly.

### ***Response to Arguments***

Applicant's arguments filed April 1, 2003 have been fully considered but they are not persuasive.

Applicant argues, on page 8 of the amendment, that Wert illustrates that the **output level** of the second drive circuit (705, 717) is higher than the **output level** of the first drive circuit and also argues that "Wert fails to teach and/or suggest driving the second drive circuit to generate a second output signal having the first **state** by a driving signal which is generated by adding a predetermined delay to the output signal." However, Examiner asserts that the word, "**voltage level**" and the word, "**state**" are interpreted in different way. For example, in TTL logic, logic high **state** is anywhere between the **voltage level** of 2.7 volts to 5 volts. Therefore, even if the **output voltage level** of the second drive circuit is higher than the **output voltage level** of the first drive circuit, the **output logic states** of the first and second drive circuits are the same since they are in logic high states.

As each and every limitation is found in the Wert reference, as explained above, the rejection of claims 1-11 and 19-29 as anticipated by Wert is maintained.

### ***Conclusion***

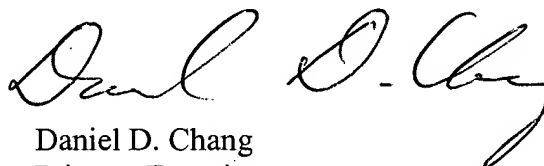
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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Daniel D. Chang whose telephone number is (703) 306-4549.

The examiner can normally be reached on Monday through Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael J. Tokar can be reached on (703) 305-3493. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 872-9318 for regular communications and (703) 872-9319 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.



Daniel D. Chang  
Primary Examiner  
Art Unit 2819

DC  
April 14, 2003

**DANIEL CHANG**  
**PRIMARY EXAMINER**